

FIG. 1

302	304	306	308	310
Header	Arithmetic code	Stop bit = 1	Alignment bi	ts = Stuffing bytes =0xff
			<u>300</u>	

FIG. 3

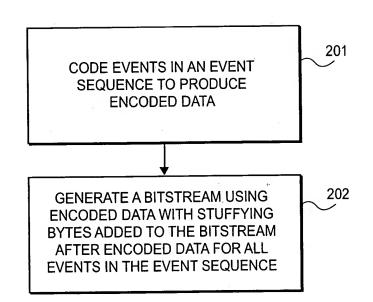


FIG. 2

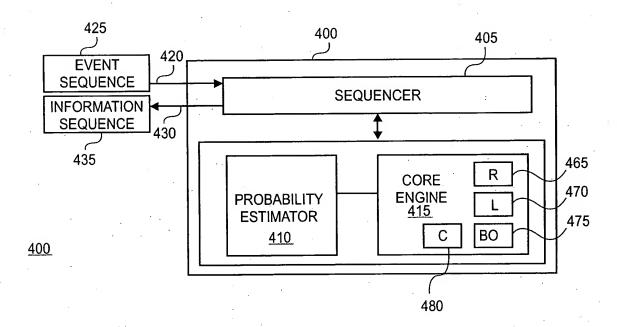


FIG. 4

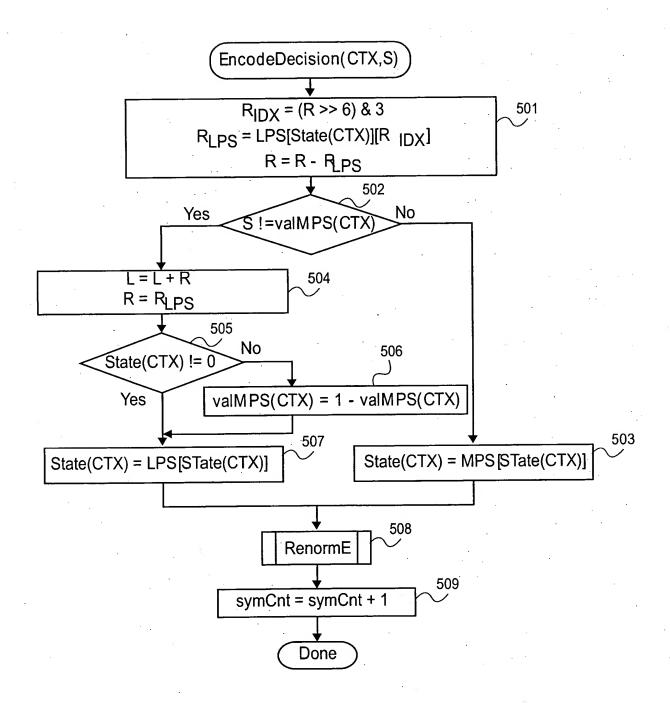


FIG. 5

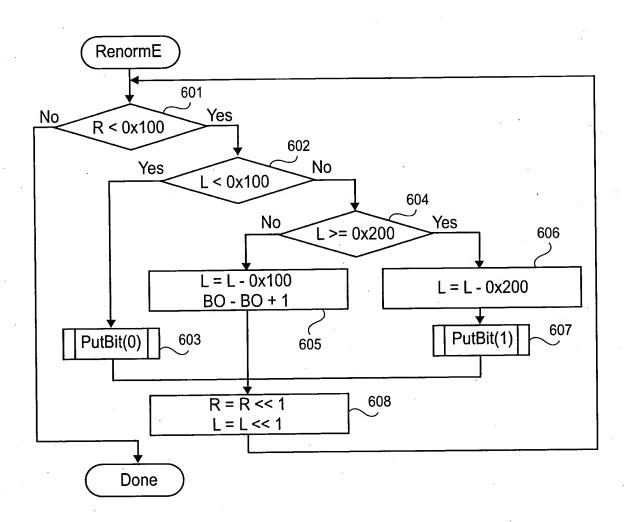


FIG. 6

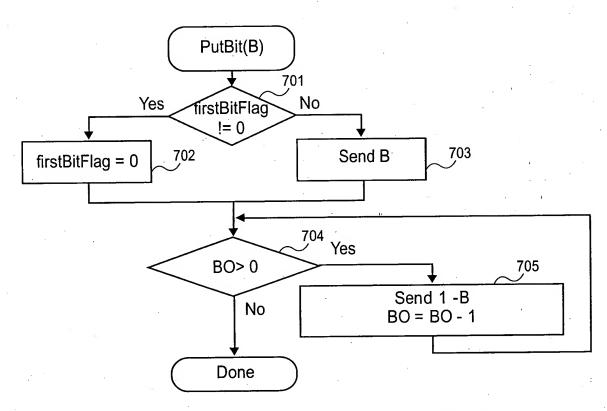
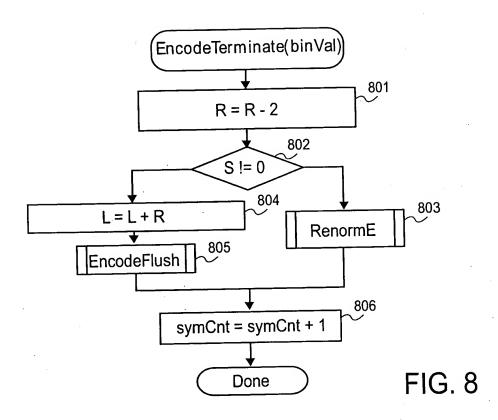


FIG. 7



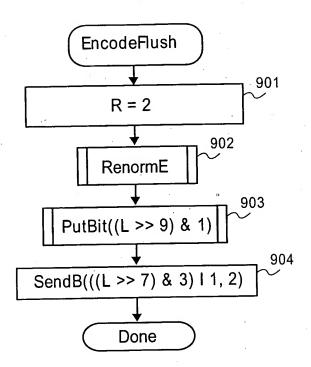


FIG. 9

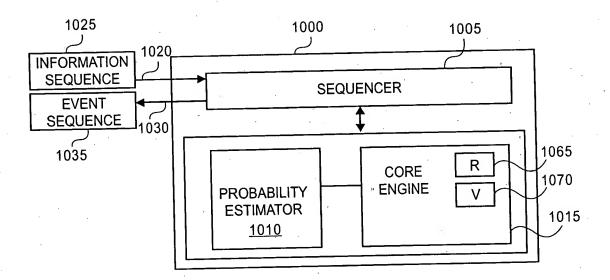


FIG. 10

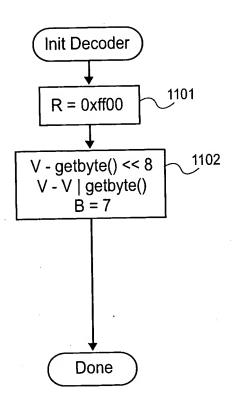
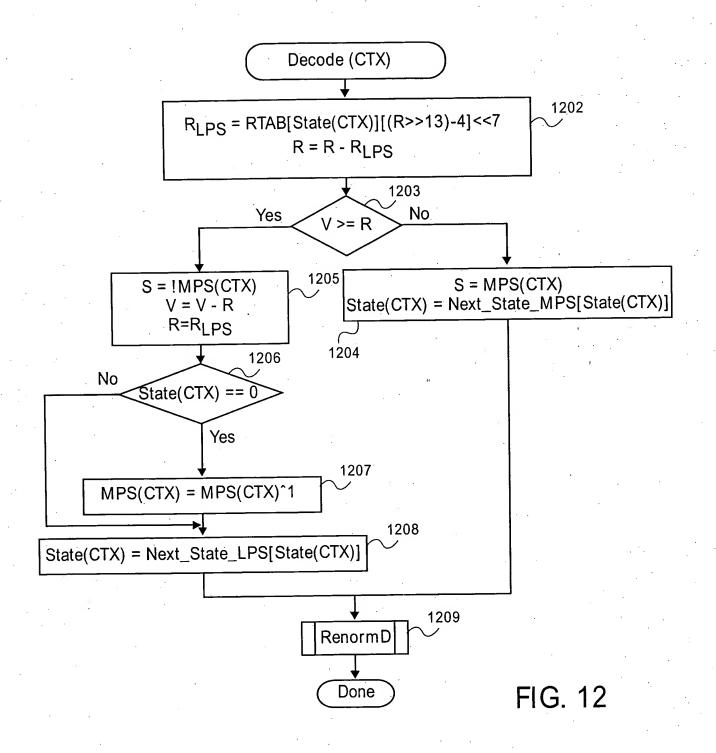


FIG. 11



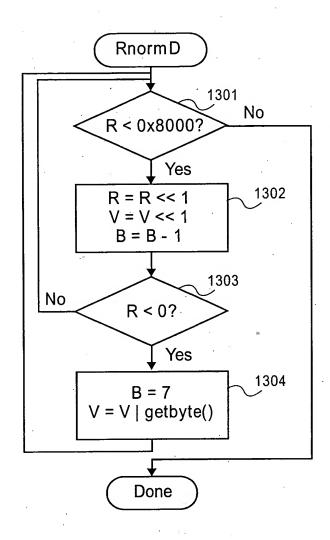
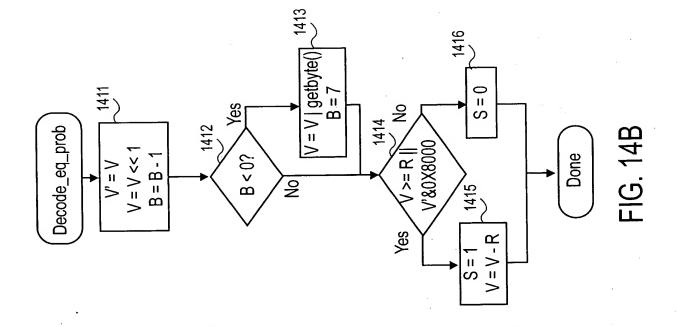
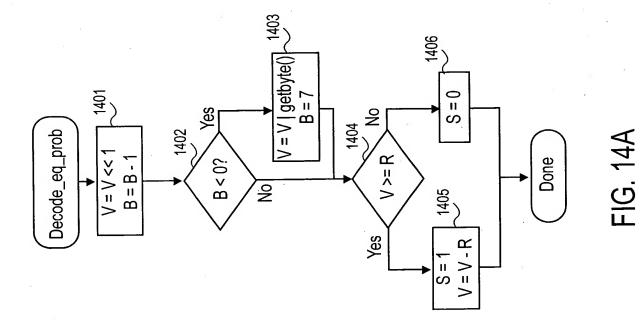


FIG. 13





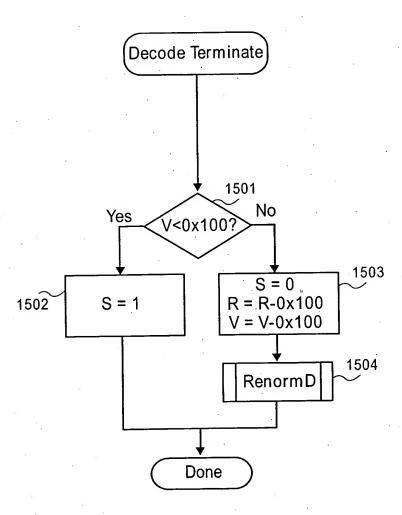


FIG. 15A

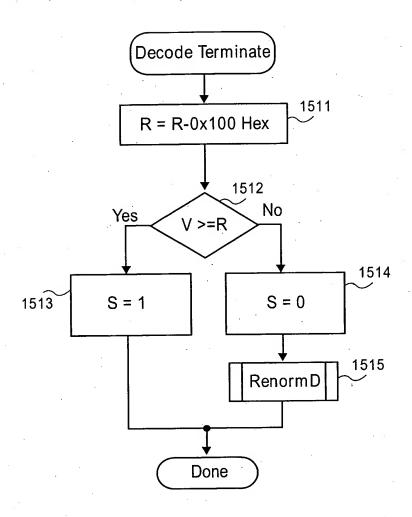


FIG. 15B

State		R <sub>I[</sub>	DX		State	R <sub>IDX</sub>						
(CTX)	0	1	2	3	(CTX)	0	1	2	3			
0	128	176	208	240	32	27	33	39	45			
1	128	167	197	227	33	26	31	37	43			
2	128	158	187	216	34	24	30	35	41			
3	123	150	178	205	35	23	28	33	39			
4	116	142	169	195	36	22	27	32	37			
5	111	135	160	185	37	21	26	30	35			
6	105	128	152	175	38	20	24	29	33			
7	100	122	144	166	39	19	23	27	31			
8	95	116	137	158	40	18	22	26	30			
9	90	110	130	150	41	17	21	25	28			
10	85	104	123	142	42	16	20	23	27			
11	81	99	117	135	43	15	19	22	25			
12	77	94	111	128	44	14	18	21	24			
13	73	89	105	122	45	14	17	20	23			
. 14	69	85	100	116	46	13	16	19	22			
15	66	80	95	110	47	12	15	18	21			
16	62	76	90	104	48	12	14	- 17	20			
17	59	72	86	99	49	11	14	16	19			
18	56	69	81	94	50	11	13	15	18			
19	53	65	77	89	51	10	12	15	17			
20	51	62	73	85	52	10	12	14	16			
21	48	59	69	80	53	9	11	13	15			
22	46	56	66	76	54	9	11	12	14			
23	43	53	63	72	55	8	10	12	14			
24	41	50	59	69	56	8	9	11	13			
25	39	48	56	65	57	7	9	11	12			
26	37	45	54	62	58	7	9	10	12			
27	35	43	51	59	59	7	8	10	11			
28	33	41	48	56	60	6	8	9	11			
29	32	39	46	53	61	6	7	9	10			
30	30	37	43	50	62	6	7	8	9			
31	29	35	41	48	63	2	2	2	2			

FIG. 16A

State(CTX)	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
transldxLPS	0	0	1	2	2	4	4	5	6	7	8	9	9	11	11	12
transldxMPS	1	2	3	4	5	6	7	8	9	·10	11	12	13	14	15	16
State(CTX)	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
transldxLPS	13	13	15	15	16	16	18	18	19	19	21	21	22,	22	23	24
transldxMPS	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
State(CTX)	32	33	34	35	36	37	38	39	, 40	41	42	43	44	45	46	47
transldxLPS	24	25	26	26	27	27	28	29	29	30	30	30	31	32	32	33
transldxMPS	33	34	35	36	37	:38	39	40	41	42	43	44	45	46	47	48
State(CTX)	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
transldxLPS	33	33	34	34	35	35	35	36	36	36	37	37	37	38	38	63
transldxMPS	49	50	51	52	53	54	55	56	57	58	59	60	61	62	62	63

FIG. 16B

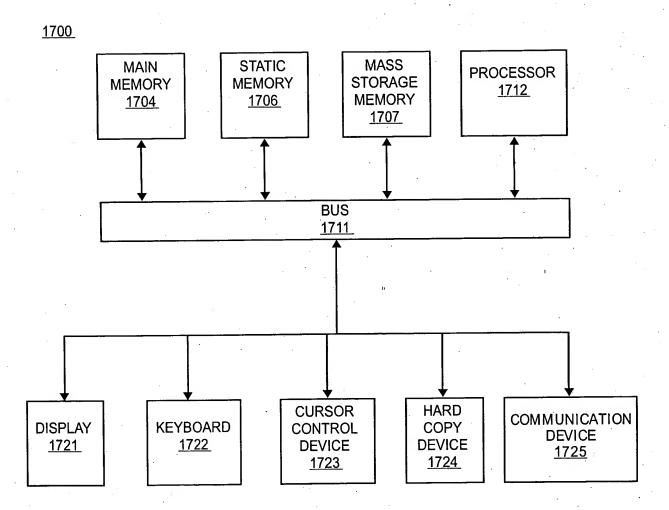


FIG. 17